

In the claims:

1. (currently amended) A semiconductor dry etching system comprising:
a plasma chamber in which at least polymerizing gas is introduced, excess polymer forming and subsequently peeling off inner vertical walls of the chamber and falling down due to gravity; and,
a electrically biased mechanism to hold a semiconductor wafer ~~over~~ at a top of the plasma chamber, such that the polymer is electrostatically attracted to the wafer, positioning of the wafer ~~over at the top of~~ the chamber preventing the excess polymer from falling onto the wafer, the electrically biased mechanism comprising:
a wafer lifter positioned at the top of the plasma chamber, having sidewalls defining a first diameter greater than a diameter of the wafer and a bottom having a hole therein having a second diameter less than the first diameter and less than the diameter of the wafer, the wafer exposed from the bottom of the wafer lifter through the hole therein.
 2. (cancelled)
 3. (currently amended) The system of claim 2 1, wherein the electrically biased mechanism further comprises a wafer chuck ~~to move the wafer upside down to over the plasma chamber.~~
 4. ~~(original) The system of claim 3, wherein the electrically biased mechanism further~~
comprises a bias supply to electrically bias at least one of the wafer chuck and the wafer lifter.
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5. (currently amended) The system of claim 4, wherein the wafer lifter is vertically movable between a lower position to an upper position, where the lower position promotes loading of the wafer ~~from the wafer chuck~~ to the wafer lifter, and the upper position enables the bias supply to electrically couple with the wafer chuck for biasing of the wafer.

6. (cancelled)

7. (original) The system of claim 1, further comprising one or more coils to induce a varying magnetic field within the chamber.

8. (currently amended) The system of claim 7, wherein the one or more coils comprise one or more induction coils ~~coupled to an inductive supply~~.

9. (currently amended) The system of claim 7, wherein the one or more coils comprise one or more electromagnetic coils ~~coupled to an electromagnetic supply~~.

10. (original) The system of claim 7, further comprising one or more multi-pole magnets cooperating with the one or more coils to assist inducement of the varying magnetic field within the chamber.

~~11. (currently amended) The system of claim 1, further comprising a dielectric window~~
~~below~~ at a bottom of the chamber.

12. (currently amended) A semiconductor dry etching system comprising:
a plasma chamber in which at least polymerizing gas is introduced;
a wafer lifter to hold a semiconductor wafer upside-down ~~over~~ at a top of the plasma chamber, the wafer lifter positioned at the top of the plasma chamber, having sidewalls defining a first diameter greater than a diameter of the wafer and a bottom having a hole therein having a second diameter less than the first diameter and less than the diameter of the wafer, the wafer exposed from the bottom of the wafer lifter through the hole therein; and,
a bias supply to bias the wafer chuck and the wafer, such that ~~the~~ polymer is electrostatically attracted to the wafer.

13. (currently amended) The system of claim 12, further comprising a wafer chuck ~~to move the wafer upside down to over the plasma chamber for the wafer lifter to hold the wafer upside down over the plasma chamber.~~

14. (cancelled)

15. (original) The system of claim 12, wherein the wafer lifter is vertically movable between a lower position to an upper position, where the lower position promotes loading of the wafer, and the upper position enables the bias supply to electrically couple with the wafer for biasing thereof.

16. (original) The system of claim 12, further comprising one or more coils to induce a varying magnetic field within the chamber.

17. (currently amended) The system of claim 16, wherein the one or more coils comprise one or more induction coils coupled ~~to an inductive supply.~~

18. (original) The system of claim 16, further comprising one or more magnets cooperating with the one or more coils to assist inducement of the varying magnetic field within the chamber.

19. (currently amended) The system of claim 12, further comprising a dielectric window below at a bottom of the chamber.

20. (withdrawn) A method comprising:

lowering a wafer lifter positioned over a plasma chamber of a semiconductor dry etching system;

loading a semiconductor wafer upside-down into the wafer lifter;

raising the wafer lifter to electrically couple the wafer with a cathode of the semiconductor dry etching system; and,

performing dry etching semiconductor processing on the wafer.
